

FIG.1

PRIOR ART

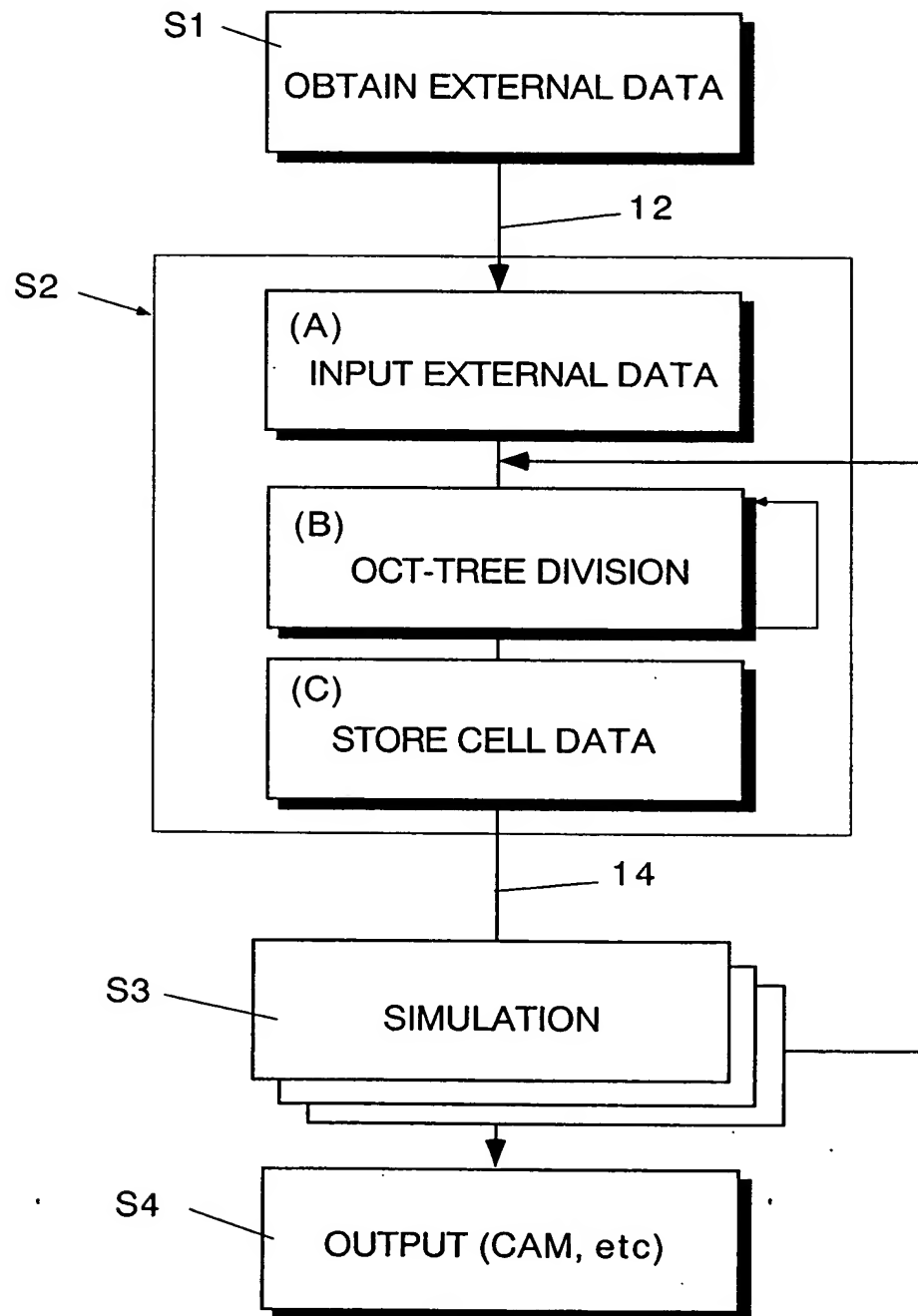


FIG. 2

## PRIOR ART

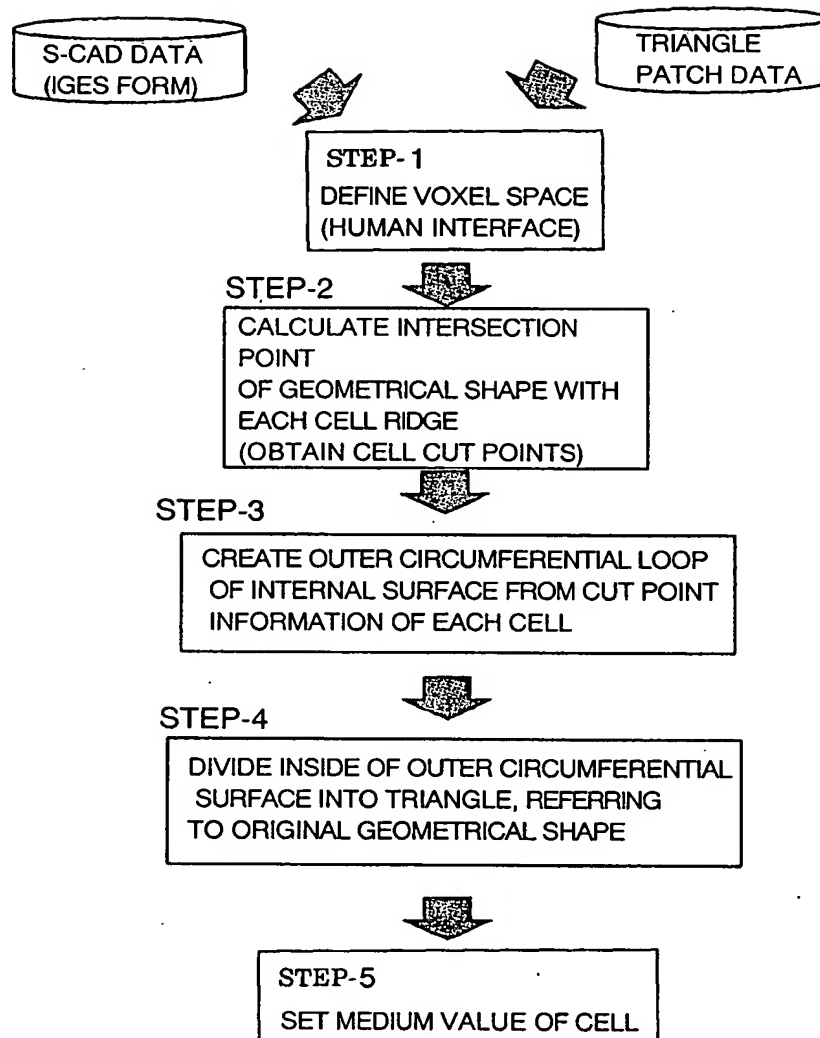


FIG. 3  
PRIOR ART

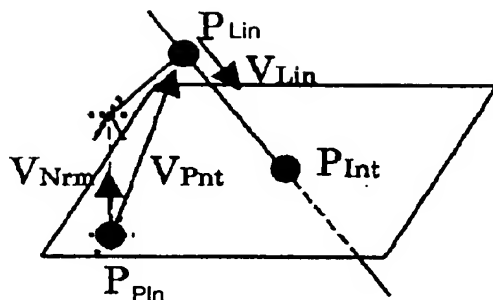


FIG. 4  
PRIOR ART

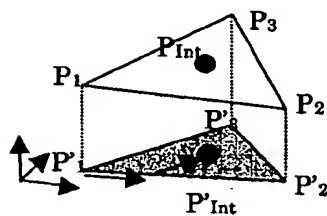


FIG. 5A  
PRIOR ART

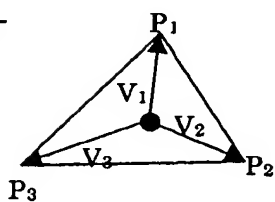


FIG. 5B  
PRIOR ART

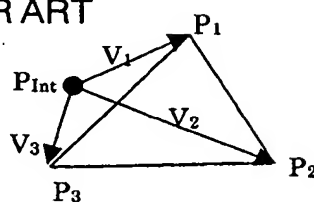


FIG. 6A  
PRIOR ART

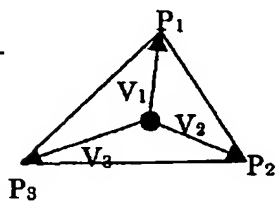


FIG. 6B  
PRIOR ART

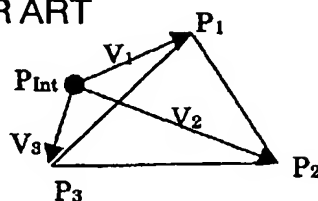
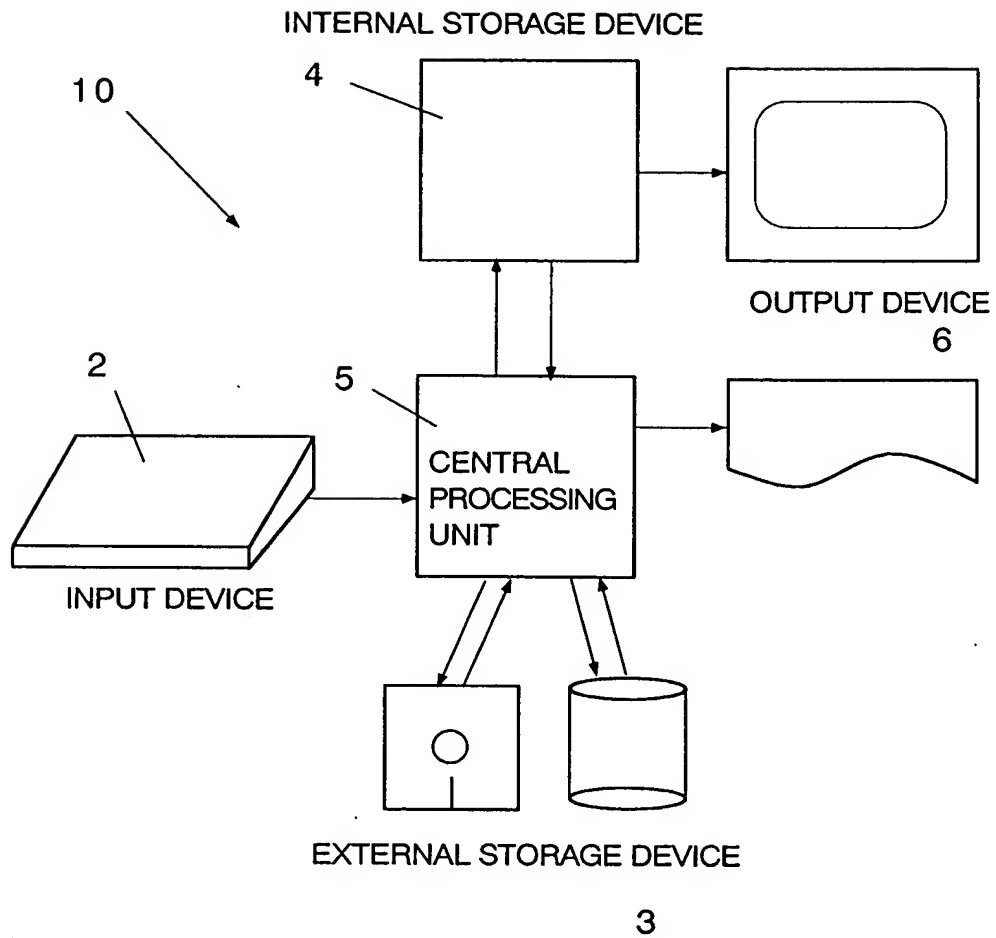
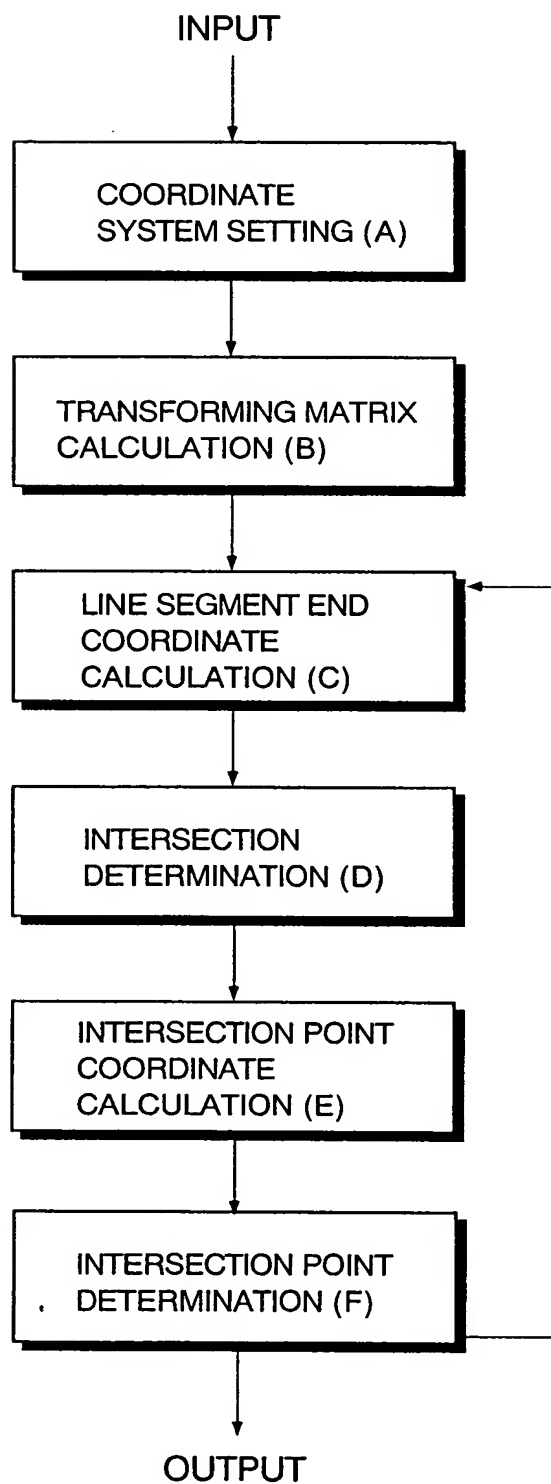


FIG. 7



5/10

FIG. 8



6/10

FIG. 9

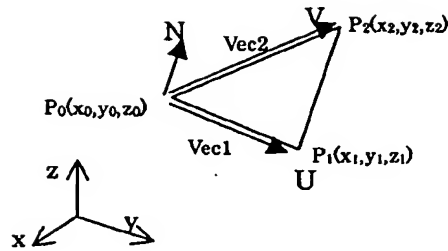
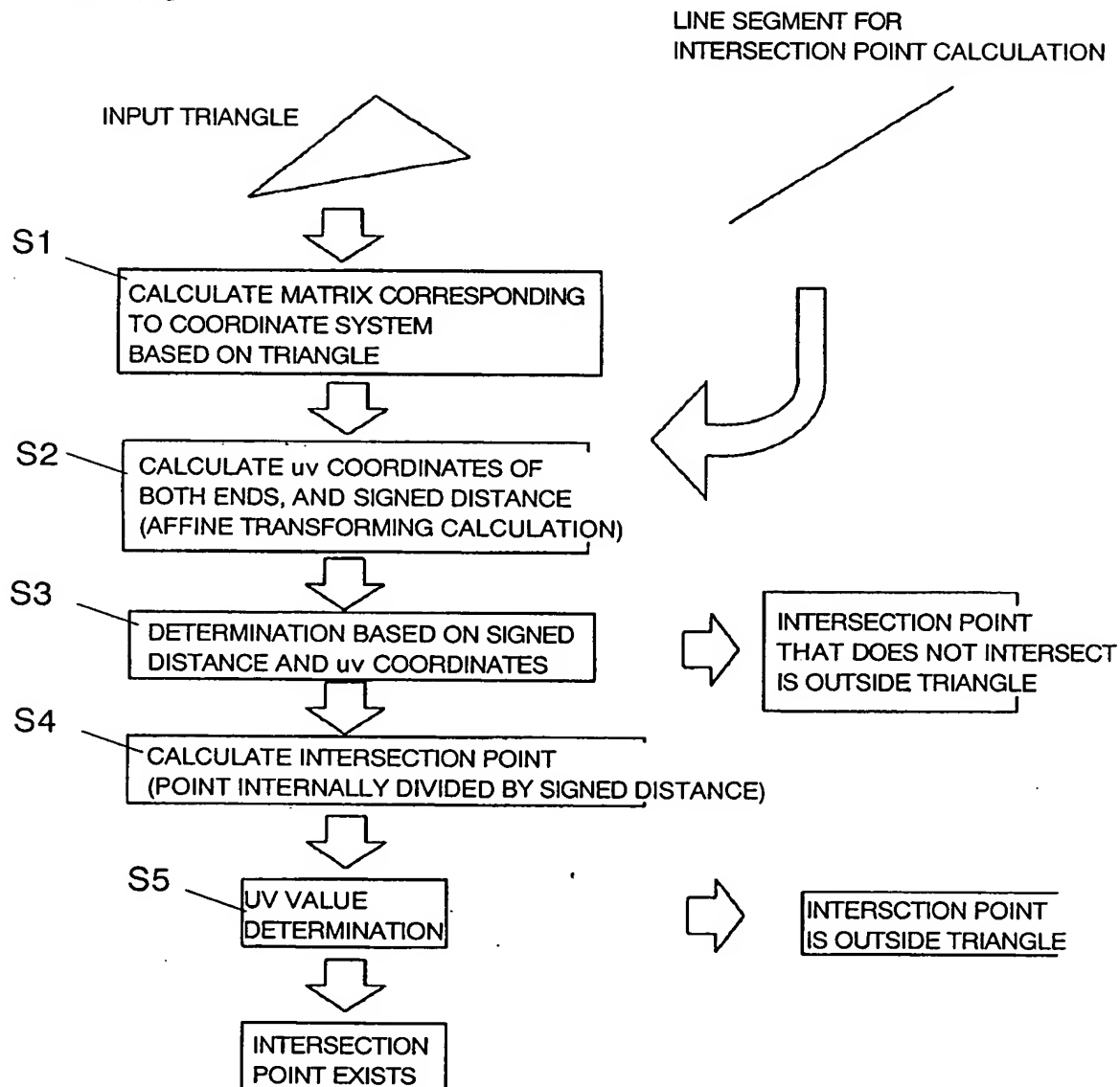


FIG. 10



7/10

FIG. 11

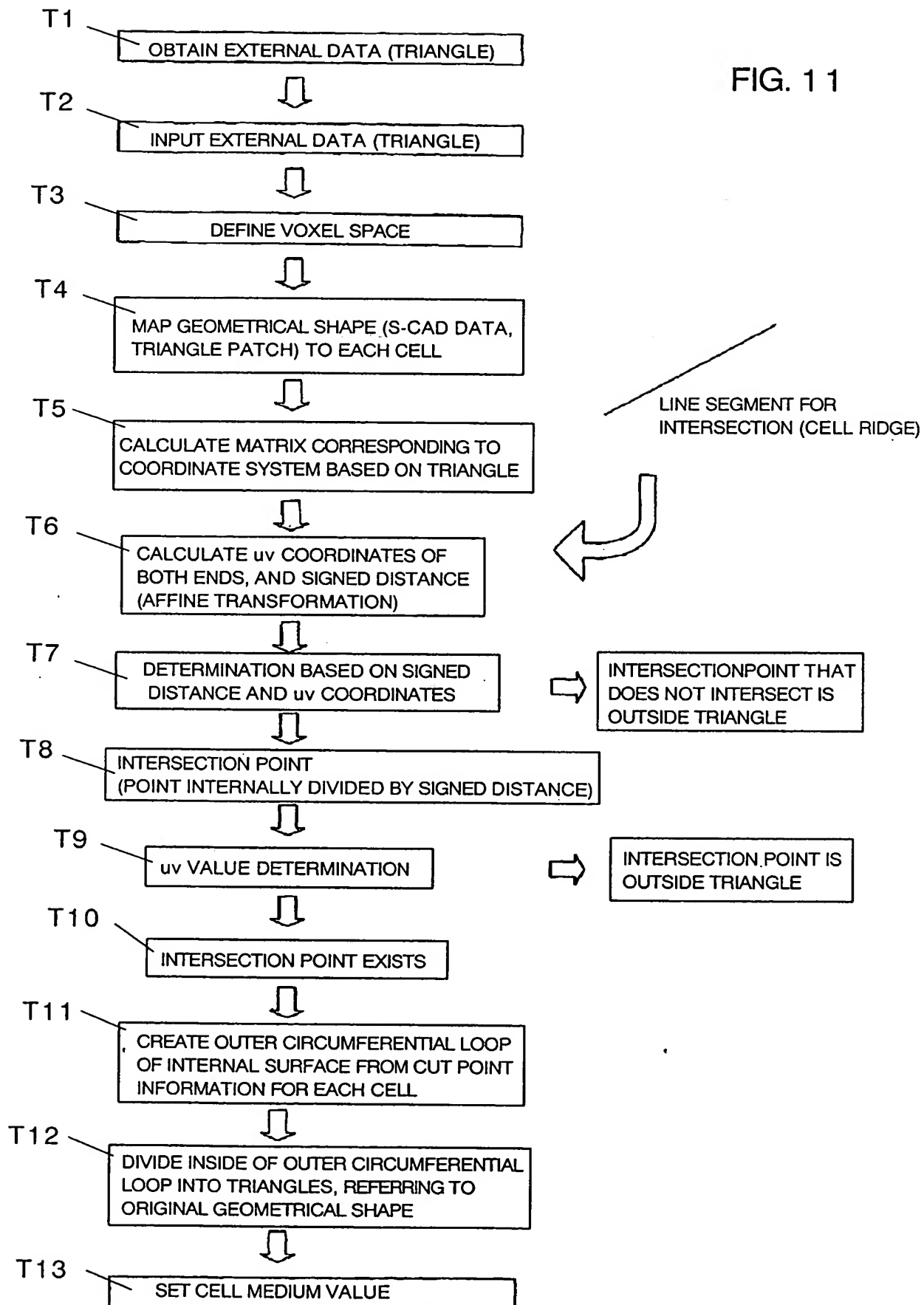
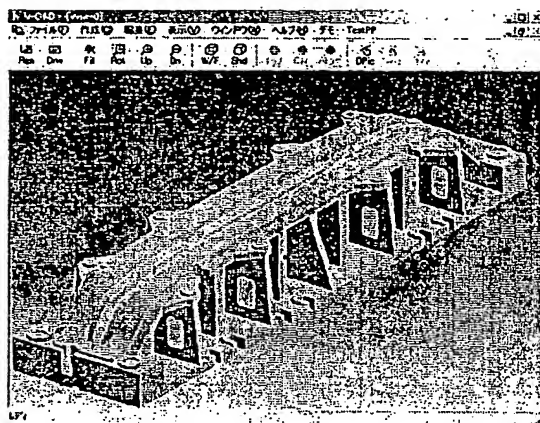


FIG. 12



DISPLAY INPUT TRIANGLE

FIG. 13

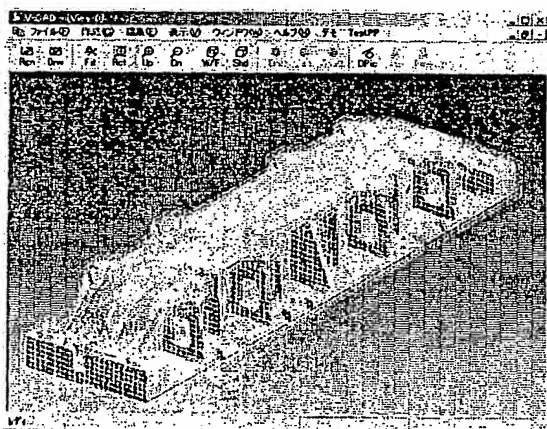
V-CAD DATA  
CONSTITUTED BY  
CELL AND  
CELL INTERNAL SURFACE

FIG. 14

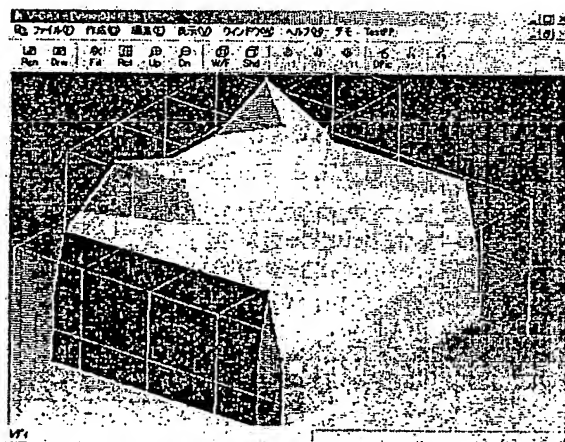
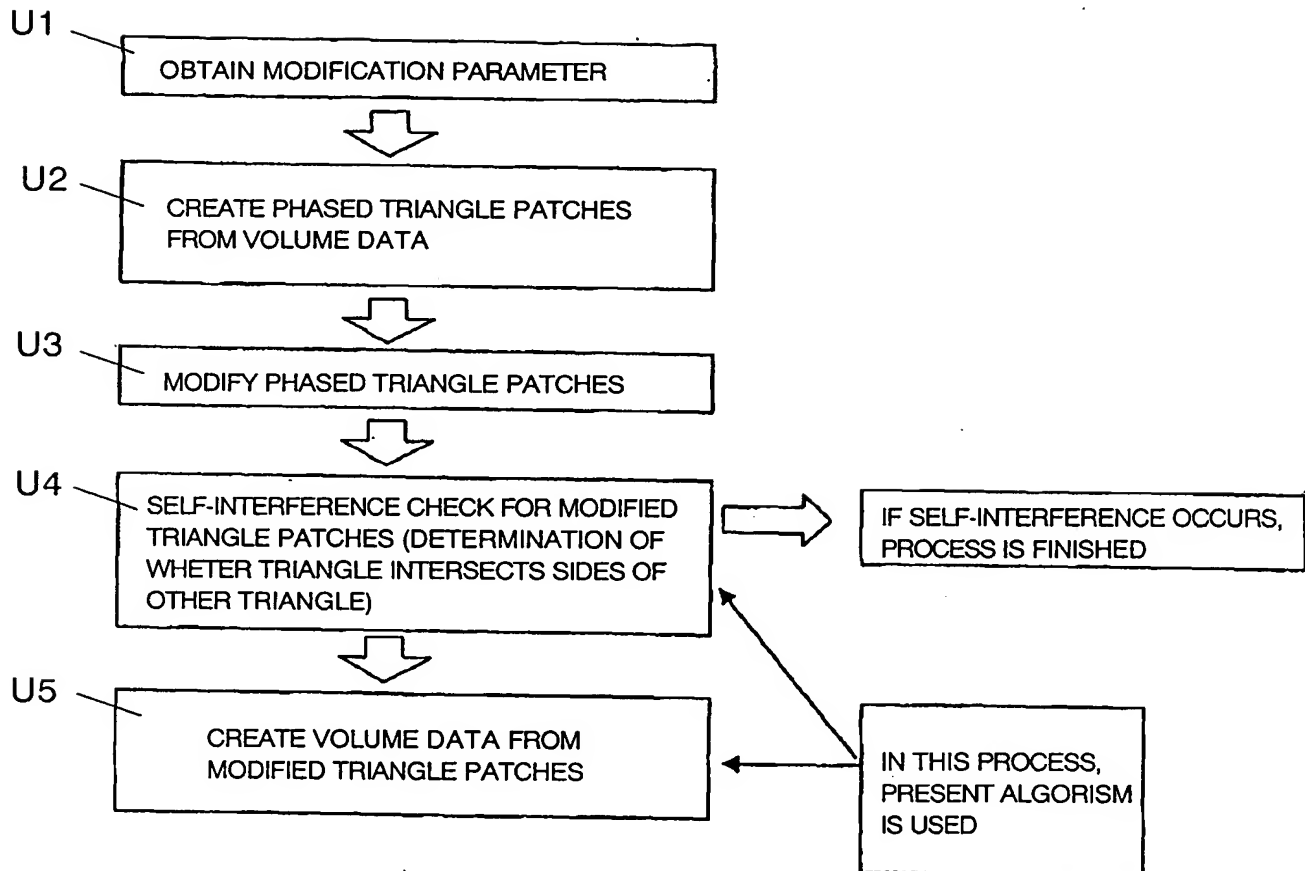
CELL CUT POINTS  
PRODUCED BY  
CALCULATING  
INTERSECTION POINT  
OF INPUT DATA  
WITH CELL RIDGE



FIG. 15



10/10

BEST AVAILABLE COPY

図16

